JAWAHARLAL NEHRU TECHNOLOGICAL UNIVERSITY ANANTAPUR

DEPARTMENT OF ELECTRONICS AND COMMUNICATION ENGINEERING
COURSE STRUCTURE AND SYLLABI FOR
M.TECH- ELECTRONICS AND COMMUNICATION ENGINEERING (ECE)
w.e.f. 2017-18 Admitted Batch onwards

M. Tech I Semester

S.No	Subject Code	Subject	L	T	P	C
1.	17D06101	Structural Digital System Design	4	-	-	4
2.	17D38102	Digital Communication Techniques	4	-	-	4
3.	17D06104	Advanced Microprocessors and Microcontrollers	4	-	-	4
4.	17D06108	Advanced Digital Signal Processing	4	-	-	4
5.	17D06210 17D06105 17D38104	**Elective-I** a. Low Power VLSI Design b. Advanced Operating Systems c. Transform Techniques	3	-	-	3
6.	17D06209 17D38206 17D06208	**Elective-II** a. Digital Image and Video Processing b. Optical Communication Technology c. Network Security and Cryptography	3	-	-	3
7.	17D38107	Structural Digital System Design Lab	-	-	3	2
8.	17D38207	Advanced Communications Systems Lab	-	-	3	2
		Total	**22**	**-**	**06**	**26**

M. Tech II Semester

S.No	Subject Code	Subject	L	T	P	C
1.	17D70201	Detection and Estimation Theory	4	-	-	4
2.	17D06201	Embedded System Design	4	-	-	4
3.	17D06204	Sensors and Actuators	4	-	-	4
4.	17D38201	Wireless Communications and Networks	4	-		4
5.	 17D38204 17D06103 17D70202	**Elective-III** a. Software Defined Radio b. Advanced Computer Architecture c. Soft Computing Techniques	3	-	-	3
6.	 17D70203 17D70204 17D38202	**Elective-IV** a. Digital Signal Processors and Architectures b. EMI / EMC c. Internet of Things	3	-	-	3
7.	17D38108	Advanced Digital Signal Processing Lab	-	-	3	2
8.	17D38208	Embedded System Design Lab	-	-	3	2
		Total	**22**	**-**	**06**	**26**

M.Tech. II YEAR (III Semester)

S. No	Course Code	Subject	L	T	P	C
1.	 17D20301 17D20302 17D20303	Elective – V (Open Elective) 1. Research Methodology 2. Human Values & Professional Ethics 3. Intellectual Property Rights	4	---	---	4
2.	17D70301	ELECTIVE – VI (MOOCs)	--	---	---	--
3.	17D70302	Comprehensive Viva Voce	--	---	---	2
4.	17D70303	Seminar	--	---	---	2
5.	17D70304	Teaching Assignment	--	---	---	2
6.	17D70305	Project Work Phase I	--	---	---	4
	Total		4			14

M.Tech. II YEAR (IV Semester)

S. No	**Course Code**	**Subject**	**L**	**T**	**P**	**C**
1.	17D70401	Project Work Phase II	--	---	---	12
	Total					**12**

Project Viva Voce Grades:

A: Satisfactory

B: Not Satisfactory

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M.Tech I year I Semester (ECE)	**L**	**T**	**P**	**C**
	4	**0**	**0**	**4**

(17D06101) STRUCTURED DIGITAL SYSTEM DESIGN

Course Objective:

- To study about structural functionality of different Digital blocks (Both combinational and Sequential)
- To provide an exposure to ASM charts, their notations and their realizations.
- To provide an exposure to VHDL and different styles of modeling using VHDL.
- To introduce concept of micro programming and study issues related to micro programming

Course Outcome:

After Completion of this course students will be able to

- Understand structural functionality of different digital blocks
- Represent and Realize their designs in ASM charts
- Represent their designs in different modelling styles by using VHDL
- Understand concept of Micro program and issues related to micro programming

UNIT-1

BUILDING BLOCKS FOR DIGITAL DESIGN: Multiplexer, Demultiplexer, Decoder, Encoder, Comparator, Adder, ALU, Carry-look-ahead adder.

BUILDING BLOCKS WITH MEMORY: Clocked building blocks, register building blocks, RAM, ROM, PLA, PAL, Timing devices.

UNIT -II

DESIGN METHODS: Elements of design style, top-down design, separation of controller and architecture, refining architecture, and control algorithm, Algorithmic State Machines, ASM chart notations.

UNIT-III

REALISING ASMS - Traditional synthesis from ASM chart, multiplexer controller method, one-shot method, ROM based method.

ASYNCHRONOUS INPUTS AND RACES - Asynchronous ASMs, Design for testability, test vectors, fault analysis tools.

UNIT-IV

MICROPROGRAMED DESIGN: Classical Microprogramming with Modem Technology; Enhancing the Control Unit; The 2910 Microprogram Sequencer; Choosing a Microprogram Memory; A Development System for Microprogramming; Designing a Microprogrammed Minicomputer

UNIT-V

MODELLING WITH VHDL: CAD tools, simulators, schematic entry, synthesis from VHDL.

DESIGN CASE STUDIES: Single pulse, system clock, serial to parallel data conversion, traffic light controller.

TEXT BOOKS:

1. Franklin P. Prosser and David E. Winkel, "The Art of Digital Design", Prentice Hall.
2. Roth, “Digital System Design using VHDL”, Mc. Graw Hill, 2000

REFERENCE BOOKS:

1. William Fletcher, An Engineering Approach to Digital Design, 1st Edition, Prentice-Hall India, 1997.
2. William J Dally and John W Poulton, Digital Systems Engineering, Cambridge University Press, 2008.
3. Jayaram Bhasker, A VHDL Primer, 3rd edition, Prentice-Hall India, 2009.
4. VHDL for Programmable Logic - Kevin Skahill, Cypress Semiconductors

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M.Tech I year I Semester (ECE)	**L**	**T**	**P**	**C**
	4	**0**	**0**	**4**

(17D38102) DIGITAL COMMUNICATION TECHNIQUES

Course Objective:

- To study about base band signal concepts and different equalizers.
- To study in detail about coherent detection schemes such as ASK, FSK, PSK
- To study in detail about M'arysignalling schemes like QPSK, QAM, MSK.

Course Outcome:

- Students will be aware of base band signal concepts and different equalizers.
- Students will be able to get complete knowledge regarding coherent detection schemes like ASK, FSK, PSK.
- Students will be able to design M'arysignalling schemes like QPSK, QAM, MSK

UNIT I

Review of Random Variables and Random Processes:

Random variable, Moment generating function, Markov's inequality, Chebyshev's inequality, Central limit theorem, Different distributions – Gaussian, Poisson, Chi square, Rayleigh, Rician; Correlation - Auto-correlation, Cross correlation, Correlation matrix; Stationary processes, Wide sense stationary processes, Gaussian &Ergodic processes, Problem solving.

UNIT II

Baseband Signal Concepts:

Baseband data transmission, Nyquist criterion for zero ISI, Correlative level coding, Data Detection, Optimum design of transmit and receive filters, Equalization - Linear, adaptive, fractionally spaced and decision feedback equalizers.

UNIT III

Digital Modulation Schemes:

Detection using matched filter – Optimum receivers for arbitrary binary signals and M'ary Orthogonal signals, Analysis of coherent detection schemes for ASK, PSK and DPSK, M'arysignalling schemes – QPSK, QAM, MSK, Performance of the data transmission schemes under AWGN. Trellis coded Modulation.

UNIT IV

Synchronization:

Receiver synchronization, costas loop, symbol synchronization, synchronization with CPM – data aided and Non aided synchronization- synchronization methods based on properties of wide sense cyclo-stationary random process – Carrier recovery circuits – Symbol clock estimation schemes.

UNIT V

Spread Spectrum Systems:

PN sequences, Generation of PN sequences, DS spread spectrum systems, FH spread spectrum systems and performance of DSSS & FHSS in AWGN – Synchronization – Jamming considerations – Commercial Applications, Cellular subsystems.

TEXT BOOKS:

1. J.G.Proakis, Digital Communication (4/e), McGraw- Hill, 2001
2. Bernard Sklar, "Digital Communications – Fundamentals &Applications,"Prentice Hall, 2001.

REFERENCE BOOKS:

1. S.Haykin, Communication Systems (4/e), Wiley,2001.
2. R.E.Zimer&R.L.Peterson : Introduction to Digital Communication, PHI, 2001.
3. G. R. Cooper & C. D. McGillem, "Modern Communications & Spread Spectrum,"McGraw Hill, 1986.
4. L.Hanzoetal, Turbo Coding, Turbo Equalization & Space-Time Coding Wiley,2002.

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M.Tech I year I Semester (ECE)	**L**	**T**	**P**	**C**
	4	**0**	**0**	**4**

(17D06104) ADVANCED MICROPROCESSORS AND MICROCONTROLLERS

OBJECTIVES:

- To expose the students to the fundamentals of microprocessor architecture.
- To introduce the advanced features in microprocessors and microcontrollers.
- To enable the students to understand various microcontroller architectures.

OUTCOME:

The student will be able to work with suitable microprocessor / microcontroller for a specific real world application

UNIT I

MICROPROCESSOR ARCHITECTURE

Instruction Set – Data formats –Addressing modes – Memory hierarchy –register file – Cache – Virtual memory and paging – Segmentation- pipelining –the instruction pipeline – pipeline hazards –instruction level parallelism – reduced instruction set –Computer principles – RISC versus CISC.

UNIT II

HIGH PERFORMANCE CISC ARCHITECTURE – PENTIUM

CPU Architecture- Bus Operations – Pipelining – Brach predication – floating point unit- Operating Modes –Paging – Multitasking – Exception and Interrupts – Instruction set – addressing modes – Programming the Pentium processor.

UNIT III

HIGH PERFORMANCE RISC ARCHITECTURE – ARM

Organization of CPU – Bus architecture –Memory management unit - ARM instruction set- Thumb Instruction set- addressing modes – Programming the ARM processor.

UNIT IV

MOTOROLA 68HC11 MICROCONTROLLERS

Instruction set addressing modes – operating modes- Interrupt system- RTC-Serial Communication Interface – A/D Converter PWM and UART.

UNIT V

PIC MICROCONTROLLER

CPU Architecture – Instruction set – interrupts- Timers- I2C Interfacing –UART- A/D Converter –PWM and introduction to C-Compilers.

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TEXT BOOKS:

1. Daniel Tabak , „“ Advanced Microprocessors” McGraw Hill.Inc., 1995
2. James L. Antonakos , “ The Pentium Microprocessor „“ Pearson Education , 1997.
3. Steve Furber , „“ ARM System –On –Chip architecture “Addision Wesley , 2000.
4. Gene .H.Miller .” Micro Computer Engineering ,” Pearson Education , 2003.
5. John .B.Peatman , “ Design with PIC Microcontroller , Prentice hall, 1997.

REFERENCE BOOKS:

1. James L.Antonakos ," An Introduction to the Intel family of Microprocessors „" Pearson Education 1999.
2. Barry.B.Brey," The Intel Microprocessors Architecture , Programming and Interfacing " , PHI,2002.
3. Valvano "Embedded Microcomputer Systems" Thomson Asia PVT LTD first reprint 2001.
4. Readings: Web links www.ocw.nit.edu www.arm.com

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	4	**0**	**0**	**4**

(17D06108) ADVANCED DIGITAL SIGNAL PROCESSING

Course outcomes: Students will be able to

- Analyze discrete-time systems in both time & transform domain and also through pole-zero placement.
- Analyze discrete-time signals and systems using DFT and FFT.
- Design and implement digital finite impulse response (FIR) filters.
- Design and implement digital infinite impulse response (IIR) filters.
- Understand and develop multirate digital signal processing systems.

UNIT –I:

Review of DFT, FFT, IIR Filters and FIR Filters:

Multi Rate Signal Processing: Introduction, Decimation by a factor D, Interpolation by a factor I, Sampling rate conversion by a rational factor I/D, Multistage Implementation of Sampling Rate Conversion, Filter design & Implementation for sampling rate conversion.

UNIT –II:

Applications of Multi Rate Signal Processing:

Design of Phase Shifters, Interfacing of Digital Systems with Different Sampling Rates, Implementation of Narrow Band Low Pass Filters, Implementation of Digital Filter Banks, Sub-band Coding of Speech Signals, Quadrature Mirror Filters, Trans-multiplexers, Over Sampling A/D and D/A Conversion.

UNIT -III:

Non-Parametric Methods of Power Spectral Estimation: Estimation of spectra from finite duration observation of signals, Non-parametric Methods: Bartlett, Welch & Blackman- Tukey methods, Comparison of all Non-Parametric methods

UNIT –IV:

Implementation of Digital Filters:

Introduction to filter structures (IIR & FIR), Frequency sampling structures of FIR, Lattice structures, Forward prediction error, Backward prediction error, Reflection coefficients for lattice realization, Implementation of lattice structures for IIR filters, Advantages of lattice structures.

UNIT –V:

Parametric Methods of Power Spectrum Estimation: Autocorrelation & Its Properties,Relation between auto correlation & model parameters, AR Models - Yule-Walker & Burg Methods, MA & ARMA models for power spectrum estimation, Finite word length effect in IIR digital Filters – Finite word-length effects in FFT algorithms.

TEXT BOOKS:

1. Digital Signal Processing: Principles, Algorithms & Applications - J.G.Proakis& D. G. Manolakis, 4th Ed., PHI.
2. Discrete Time Signal Processing - Alan V Oppenheim & R. W Schaffer, PHI.
3. DSP – A Practical Approach – Emmanuel C. Ifeacher, Barrie. W. Jervis, 2 Ed., Pearson Education.

REFERENCE BOOKS:

1. Modern Spectral Estimation: Theory & Application – S. M .Kay, 1988, PHI.
2. Multi Rate Systems and Filter Banks – P.P.Vaidyanathan – Pearson Education.
3. Digital Signal Processing – S.Salivahanan, A.Vallavaraj, C.Gnanapriya, 2000,TMH
4. Digital Spectral Analysis – Jr. Marple

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(17D06210) LOW POWER VLSI DESIGN
Elective-I

Course Outcomes:

After completion of this subject, students will be able to

- Understand the concepts of velocity saturation, Impact Ionization and Hot Electron Effect
- Implement Low power design approaches for system level and circuit level measures.
- Design low power adders, multipliers and memories for efficient design of systems.

UNIT –I:

Fundamentals:

Need for Low Power Circuit Design, Sources of Power Dissipation – Switching Power Dissipation, Short Circuit Power Dissipation, Leakage Power Dissipation, Glitching Power Dissipation, Short Channel Effects –Drain Induced Barrier Lowering and Punch Through, Surface Scattering, Velocity Saturation, Impact Ionization, Hot Electron Effect.

UNIT –II:

Low-Power Design Approaches:

Low-Power Design through Voltage Scaling – VTCMOS circuits, MTCMOS circuits, Architectural Level Approach –Pipelining and Parallel Processing Approaches. Switched Capacitance Minimization Approaches: System Level Measures, Circuit Level Measures, Mask level Measures.

UNIT –III:

Low-Voltage Low-Power Adders:

Introduction, Standard Adder Cells, CMOS Adder's Architectures – Ripple Carry Adders, Carry Look Ahead Adders, Carry Select Adders, Carry Save Adders, Low-Voltage Low-Power Design Techniques – Trends of Technology and Power Supply Voltage, Low-Voltage Low-Power Logic Styles.

UNIT –IV:

Low-Voltage Low-Power Multipliers:

Introduction, Overview of Multiplication, Types of Multiplier Architectures, Braun Multiplier, Baugh Wooley Multiplier, Booth Multiplier, Introduction to Wallace Tree Multiplier.

UNIT –V:

Low-Voltage Low-Power Memories:

Basics of ROM, Low-Power ROM Technology, Future Trend and Development of ROMs, Basics of SRAM, Memory Cell, Precharge and Equalization Circuit, Low-Power SRAM Technologies, Basics of DRAM, Self-Refresh Circuit, Future Trend and Development of DRAM.

TEXT BOOKS:

1.CMOS Digital Integrated Circuits – Analysis and Design – Sung-Mo Kang, Yusuf Leblebici, TMH, 2011.
2. Low-Voltage, Low-Power VLSI Subsystems – Kiat-Seng Yeo, Kaushik Roy, TMH Professional Engineering.

REFERENCE BOOKS:

1. Introduction to VLSI Systems: A Logic, Circuit and System Perspective – Ming-BO Lin, CRC Press, 2011.
2. Low Power CMOS Design – AnanthaChandrakasan, IEEE Press/Wiley International, 1998.
3. Low Power CMOS VLSI Circuit Design – Kaushik Roy, Sharat C. Prasad, John Wiley & Sons, 2000.

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(17D06105) ADVANCED OPERATING SYSTEMS

Elective-I

Course Objective:

- To Study in detail about kernel structures associated with various Operating systems
- To Study in detail about various systems calls, statements and their arguments associated with Unix .
- To Study in detail about various systems calls, statements and their arguments associated with Linux

Course Outcome:

After completion of the course students will be able to

- Get complete knowledge regarding different types of operating systems and their Kernel structures.
- To work effectively on Unix Platform
- To work effectively on Linux Platform

UNIT I

INTRODUCTION

General Overview of the System : History – System structure – User perspective – Operating system services – Assumptions about hardware. Introduction to the Kernel : Architecture of the UNIX operating system – Introduction to system concepts. The Buffer Cache: Buffer headers – Structure of the buffer pool – Scenarios for retrieval of a buffer – Reading and writing disk blocks – Advantages and disadvantages of the buffer cache.

UNIT II

UNIX I: Overview of UNIX system, Structure, files systems, type of file, ordinary & Special files, file permissions, Introduction to shell.UNIX basic commands & command arguments, Standard input / output Input / output redirection, filters and editors,System calls related file structures, input / output process creation & termination.

UNIT III

INTERPROCESS COMMUNICATION IN UNIX: Introduction, file and record locking, Client – Server example, pipes, FIFOs, Streams & Messages, Name Speces, Systems V IPC, Message queues, Semaphores, Shared Memory, Sockets & TLI.

UNIT IV

INTRODUCTION TO NETWORKS AND NETWORK PROGRAMMING IN UNIX: Network Primer, TCP/IP, Internet Protocols, Socket Programming, Introduction & overview, UNIX domain protocols, Socket Addresses, Elementary Socket system calls, Simple examples.

UNIT V

LINUX: Introduction to LINUX System, Editors and Utilities, Type of Shells,Shell Operations, File structure, File Management, Operations. Memory Management Policies: Swapping – Demand paging. The I/O Subsystem: Driver Interface – Disk Drivers – Terminal Drivers– Streams – Inter process communication.

TEXT BOOKS:

1. Maurice J.Bach, “The design of the UNIX Operating Systems”, PHI
2. Kernighan & Pike, “The UNIX Programming Environment”, PHI

REFERENCE BOOKS:

1. W.Richard Stevens, “UNIX Network Programming”, PHI, 1998.
2. Richard Peterson, “The Complete reference LINUX”, TMH
3. Ritchie & Yates, “UNIX User Guide”.

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M.Tech I year I Semester (ECE)	**L**	**T**	**P**	**C**
	3	**0**	**0**	**3**

(17D38104) TRANSFORM TECHNIQUES
Elective-I

Course Objective:

- Study of different types of transforms which can be applicable for different types of signals.
- To study the application of wavelets for different types of signals.
- To study the applications of Multi rate systems and filter banks.

Course Outcome:

After completion of the course the student will be able to

- Use different 1-d and 2-d transforms for different signals.
- Apply wavelet transforms for different signals and will be able to appreciate its differences with other transformations.
- Use different advanced transforms such as DCT, DWT and KLT for different applications like signal de noisy, sub band coding of speech and music and signal compression.

UNIT I:

REVIEW OF TRANSFORMS: Signal spaces, concept of convergence, Hilbert spaces for energy signals, Orthogonality, Ortho normality, Fourier basis, FT-failure of FT-need for time-frequency analysis, spectrogram plot-phase space plot in time-frequency plane, Continuous FT, DTFT, Discrete Fourier Series and Transforms, Z-Transform.

ADVANCE TRANSFORMS

Relation between CFT-DTFT, DTFT-DFS, DFS-DFT, DCT (1D&2D), Walsh, Hadamard, Haar, Slant, KLT, Hilbert Transforms – definition, properties and applications.

UNIT II:

CWT & MRA: Time-frequency limitations, tiling of time-frequency plane for STFT, Heisenberg uncertainty principle,Short time Fourier Transform (STFT) analysis, short comings of STFT.

NEED FOR WAVELETS: Wavelet Basis- Concept of Scale and its relation with frequency, Continuous time wavelet Transform Equation- Series Expansion using Wavelets- CWT.

UNIT III:

NEED FOR SCALING FUNCTION: Multi resolution analysis, Tiling of time scale plane for CWT. Important Wavelets: Haar, Mexican Hat Meyer, Shannon, Daubechies.

SPECIAL TOPICS: Wavelet Packet Transform, Bi-orthogonal basis- B-splines, Lifting Scheme of Wavelet Generation-implementation.

UNIT IV:

MULTIRATE SYSTEMS, FILTER BANKS AND DWT: Basics of Decimation and Interpolation in time & frequency domains, Two-channel Filter bank, Perfect Reconstruction Condition, Relationship between Filter Banks and Wavelet basis, DWT Filter Banks for Daubechies Wavelet Function.

UNIT V:

APPLICATIONS OF TRANSFORMS: Signal De-noising, Sub-band Coding of Speech and Music, Signal Compression - Use of DCT, DWT, KLT.

TEXT BOOKS:

1. Jaideva C Goswami, Andrew K Chan, "Fundamentals of Wavelets- Theory, Algorithms and Applications", John Wiley & Sons, Inc, Singapore, 1999.
2. RaghuveerM.Rao and Ajit S. Bopardikar, "Wavelet Transforms-Introduction theory and applications" Pearson edu, Asia, New Delhi, 2003.
3. Soman.K.P, Ramachandran K.I, "Insight into Wavelets from Theory to practice", Printice Hall India, First Edition, 2004.

REFERENCE BOOKS:

1. Vetterli M. Kovacevic, "Wavelets and sub-band coding", PJI, 1995.
2. C. Sydney Burrus, "Introduction to Wavelets and Wavelet Transforms", PHI, First Edition, 1997.
3. Stephen G. Mallat, "A Wavelet Tour of Signal Processing", Academic Press, Second Edition,
4. Jayaraman, "Digital Image Processing", TMH,2009
5. S.Jayaraman, S.Esakkirajan, T.Veera Kumar, "Digital Image Processing", TMH,2009

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M.Tech I year I Semester (ECE)	**L**	**T**	**P**	**C**
	3	**0**	**0**	**3**

(17D06209) DIGITAL IMAGE AND VIDEO PROCESSING
Elective-II

OBJECTIVES:

- To provide the basic concepts of image & pattern recognition.
- To give an exposure to basic image processing and modeling techniques.
- To provide an understanding of various concepts related to video object extraction.
- To prepare students for development and implementation of algorithms

OUTCOMES:

- To be able to design pattern recognition systems.
- To design and implement feature extraction techniques for a given application.
- To design a suitable classifier for a given application.

UNIT-I
IMAGE FUNDAMENTALS AND TRANSFORMS
Image Representation- Sampling and Quantization - Two dimensional DFT- Discrete cosine Transform - Walsh - Hadamard transform - Wavelet transform - Construction of Wavelets-Types of wavelets - principal component analysis.

UNIT -II
PROCESSING AND MODELING OF IMAGES
Pre-processing -Point operations – contrast stretching – Histogram - Histogram equalization - Image segmentation- pixel based, edge based, region based segmentation - Morphological image processing - Edge and texture models - Image registration - Colour Image Processing –

UNIT-III
SPATIAL FEATURE EXTRACTION
Feature selection - Localized feature extraction- Boundary Descriptors - Moments - Texture Descriptors - Co-occurrence features

UNIT-IV
CLASSIFIERS
Kernel based approaches - clustering methods - Maximum Likelihood Estimation- Bayesian approach- Pattern Classification

UNIT-V
VIDEO OBJECT EXTRACTION
Back ground subtraction – Frame difference - Static and dynamic background modeling - optical flow techniques-Handling occlusion- scale and appearance changes - Shadow removal.

TEXT BOOKS:

1. A.K.Jain, “Fundamentals of Digital Image Processing”, Prentice-Hall, 2002.
2. R.C.Gonzalez and R.E.Woods, „Digital Image Processing‟, Second Edition, Pearson Education, 2002.
3. A.Bovik, “Handbook of Image and Video Processing”, 2nd Edition, Academic Press, 2005.

REFERENCE BOOKS:

1. Mark Nixon and Alberto Aguado, “Feature Extraction and Image Processing”, Academic Press, 2008.
2. John C.Russ, “The Image Processing Handbook”, CRC Press, 2007.
3. Richard O. Duda, Peter E. Hart and David G. Stork., “Pattern classification”, Wiley, 2001

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M.Tech I year I Semester (ECE)	**L**	**T**	**P**	**C**
	3	**0**	**0**	**3**

(17D38206) OPTICAL COMMUNICATION TECHNOLOGY

Elective-II

Course Outcomes:

- Distinguish Step Index, Graded index fibers and compute mode volume.
- Explain the Transmission Characteristics of fiber and Manufacturing techniques of fiber/cable.
- Classify the construction and characteristics of optical sources and detectors.
- Discuss splicing techniques, passive optical components and explain noise in optical system.
- Design short haul and long haul Analog/ Digital optical communication system and explain advanced optical transmission systems

UNIT –I:

Signal propagation in Optical Fibers:

Geometrical Optics approach and Wave Theory approach, Loss and Bandwidth, Chromatic Dispersion, Non Linear effects- Stimulated Brillouin and Stimulated Raman Scattering, Propagation in a Non-Linear Medium, Self-Phase Modulation and Cross Phase Modulation, Four Wave Mixing, Principle of Solitons.

UNIT –II:

Fiber Optic Components for Communication & Networking:

Couplers, Isolators and Circulators, Multiplexers, Bragg Gratings, Fabry-Perot Filters, Mach Zender Interferometers, Arrayed Waveguide Grating, Tunable Filters, High Channel Count Multiplexer Architectures, Optical Amplifiers, Direct and External Modulation Transmitters, Pump Sources for Amplifiers, Optical Switches and Wavelength Converters.

UNIT –III:

Modulation and Demodulation:

Signal formats for Modulation, Subcarrier Modulation and Multiplexing, Optical Modulations –
Duobinary, Single Side Band and Multilevel Schemes, Ideal and Practical receivers for Demodulation, Bit Error Rates, Timing Recovery and Equalization, Reed-Solomon Codes for Error Detection and Correction.

UNIT -IV:

Transmission System Engineering:

System Model, Power Penalty in Transmitter and Receiver, Optical Amplifiers, Crosstalk and Reduction of Crosstalk, Cascaded Filters, Dispersion Limitations and Compensation Techniques.

UNIT –V:

Fiber Non-linearities and System Design Considerations:

Limitation in High Speed and WDM Systems due to Non-linearities in Fibers, Wavelength Stabilization against Temperature Variations, Overall System Design considerations – Fiber Dispersion, Modulation, Non-Linear Effects, Wavelengths, All Optical Networks.

TEXT BOOKS:

1. Optical Networks: A Practical Perspective - Rajiv Ramaswami and Kumar N.
2. Sivarajan, 2nd Ed., 2004, Elsevier Morgan Kaufmann Publishers (An Imprint of Elsevier).
   Optical Fiber Communications – Gerd Keiser, 3rd Ed., 2000, McGraw Hill.

REFERENCE BOOKS:

1. Optical Fiber Communications: Principles and Practice – John.M.Senior, 2nd Ed., 2000, PE.
2. Fiber Optics Communication – Harold Kolimbris, 2nd Ed., 2004, PEI
3. Optical Networks: Third Generation Transport Systems – Uyless Black, 2nd Ed., 2009, PEI
4. Optical Fiber Communications – GovindAgarwal, 2nd Ed., 2004, TMH.
5. Optical Fiber Communications and Its Applications – S.C.Gupta, 2004, PHI

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(17D06208) NETWORK SECURITY AND CRYPTOGRAPHY
Elective-II

Course Objective:

- To study about need and role of security and cryptography in computer networks.
- To study about different techniques associated with encryption.
- To study about different algorithms associated with computer networks.
- To study about different security architecture and designing issues related to fire walls.

Course Outcome:

After completion of this course students will be able to know

- The need and role of security and cryptography in computer networks.
- Gain knowledge about different techniques associated with encryption.
- Functioning of different algorithms associated with computer networks.
- Gain knowledge regarding different security architecture and designing issues related to fire walls.

UNIT – I

Introduction: Attacks, services and mechanisms, security attacks, security services, a model for internet work security, protection through cryptography, the role of cryptography in network security.

UNIT – II

Conventional Encryption: Substitution techniques and transposition techniques, block cipher principles, block cipher design principles, block cipher modes of operation. The data encryption standard

UNIT – III

Public-key encryption: Principles of public-key cryptosystems, the RSA algorithm, key management. Authentication requirements, authentication functions, message authentication codes, hash functions.

UNIT – IV

Digital Signatures and Authentication Protocols: Digital signatures, Digital signature standard, Authentication Protocols, MD5, message digest algorithm, secure hash algorithm, HMAC.

UNIT – V

Mall security & IP security: Pretty good privacy, IP security overview, IP security architecture, Intruders, viruses and related threats, firewall design principles

TEXT BOOKS:

1. W. Stallings**, "**Cryptography & Network Security", 3/e, PHI, 2003
2. Eric Maiwald**, "**Fundamental of Network Security", Dreamtech press Osborne MGH, 2004
3. Sean Convery, " Network Security Architectures, Published by Cisco Press, First Ed. 2004.
4. AtulKahate, "Cryptography and Network Security", Tata McGraw Hill, 2003.

REFERENCE BOOKS:

1. Bruce Schneier, “Applied Cryptography”, John Wiley and Sons Inc, 2001.
2. Stewart S. Miller, “Wi-Fi Security”, McGraw Hill, 2003.
3. Charles B. Pfleeger, Shari Lawrence Pfleeger, “Security In Computing”, 3rd Edition, Pearson Education, 2003.
4. Jeff Crume, “Inside Internet Security” Addison Wesley, 2005.

JAWAHARLAL NEHRU TECHNOLOGICAL UNIVERSITY ANANTAPUR

M.Tech I year I Semester (ECE)	**L**	**T**	**P**	**C**
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(17D38107) STRUCTURAL DIGITAL SYSTEM DESIGN LAB

Course Objective:

- To understand about VHDL and Verilog Programming in all available styles.
- To understand differences between Verilog and VHDL.
- To represent the different digital blocks in verilog and VHDL in all available styles of modelling

Course Outcome:

After completion of this course the students will be able to understand

- Different modeling styles available in VHDL and Verilog and difference between them
- Difference between verilog and VHDL
- Representation of different digital modules in different modelling styles available in VHDL and Verilog

Using VHDL or Verilog do the following experiments

1. Design of 4-bit adder / subtractor
2. Design of Booth Multiplier
3. Design of 4-bit ALU
4. Design SISO, SIPO, PISO, PIPO Registers
5. Design of Ripple, Johnson and Ring counters
6. Design of MIPS processor
7. Design of Washing machine controller
8. Design of Traffic Light Controller
9. Design “1010” pattern detector using Mealy state Machine
10. Design “1100” recursive pattern detector using Moore state Machine
11. Design simple Security System Using FSM/ASM
12. Mini Project

Tools Required:

VHDL or VERILOG

Hardware Required:

Computers with latest Configuration.

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(17D38207) ADVANCED COMMUNICATION SYSTEMS LAB

Note:
A. Minimum of 10 Experiments have to be conducted
B. All Experiments may be Simulated using MATLAB and to be verified using related training kits.

1. Measurement of Bit Error Rate using Binary Data
2. Determination of output of convolution Encoder for a given sequence
3. Determination of output of convolution Decoder for a given sequence
4. Efficiency of Direct Sequence Spread Spectrum Technique
5. Simulation of Frequency Hopping (FH) Spread- Spectrum
6. Implementation of optimum receiver for the AWGN channel.
7. Measurement of effect of Inter Symbol Interference.
8. Design of FSK system
9. BPSK Modulation and Demodulation techniques
10. DQPSK Modulation and Demodulation techniques
11. 8-QAM Modulation and Demodulation techniques
12. OFDM Transceiver design
13. Performance evaluation of CDMA system
14. Implementation of QPSK Modulation with Rayleigh Fading and AWGN channel

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M.Tech I year II Semester (ECE)	**L**	**T**	**P**	**C**
	4	**0**	**0**	**4**

(17D70201) DETECTION AND ESTIMATION THEORY

Course Objective:

1. To provide knowledge about various estimation, and detection techniques.
2. To analyze different methods & to detect and estimate the signal from noisy signal.
3. Estimate and detect the signals in the presence of noise.

Course Outcome:

1. The students will be able to apply various methods of signal estimation knowing the significance of each method.
2. The students will be able to know Cramer-Rao Lower bound in estimating a signal.
3. By applying suitable criterion the students will be able to detect the signals with minimum errors in the presence of noise.

UNIT - I

Introduction to Estimation and Detection:

Introduction, Detection and Estimation in Signal Processing, the Mathematical Detection& Estimation problem, Assessing Estimator Performance, Hierarchy of detection problems, Role of asymptotics.

UNIT - II

Minimum Variance Unbiased Estimation:

Unbiased Estimators, Minimum Variance Criterion, Existence of the minimum Variance Unbiased Estimator, Finding the Minimum Variance Unbiased Estimator,

Cramer-Rao Lower Bound - Estimator of Accuracy Considerations, Cramer-Rao Lower Bound (CRLB), General CRLB for Signals in White Gaussian Noise, Transformation of Parameters, Extension to a Vector Paramter, Vector Parameter CRLB for Transformations, CRLB for the general Gaussian case, **Linear Models** -Definition and Properties, Linear Model Examples, Extension to the Linear Model,

General Minimum Variance Unbiased Estimation: Introduction, Sufficient Statistics, Finding Sufficient Statistics.

UNIT - III

Best Linear Unbiased Estimators:

Definition of BLUE, Finding the BLUE, Extension to Vector Parameter,

Estimation Methods - Maximum Likelihood Estimation (MLE), Finding MLE, Properties of MLE, MLE for Transformed Parameters, Numerical Determination of the MLE, Extension to a Vector Parameter, The Least Squares Approach, Linear Least Squares, Method of Moments, Extension to a Vector Parameter, Statistical Evaluation of Estimators.

The Basian Philosophy - Prior Knowledge and Estimation, Choosing a Prior PDF, Properties of Gaussian PDF, Basian Linear Model, Minimum Mean Square Error (MMSE) Estimators, Maximum A Posteriori Estimators, Performance Description, Linear Basian Estimators – Introduction, Linear MMSE Estimation, Geometrical Interpretations, The Vector LMMSE Estimator.

UNIT - IV

Statistical Decision Theory I:

Introduction, Neyman-Pearson Theorem, Receiver Operating Characteristics, Minimum Probability of Error, Bayes Risk, Multiple Hypothesis Testing,

Deterministic Signals - Matched Filters, Development of Detector, Performance of Matched Filter, Performance of Generalized Matched Filters, Multiple Signals – Binary Case and its performance, M-ary Case, Linear Model, **Random Signals**– EstimatorCorrelator, Linear Model.

UNIT - V

Statistical Decision Theory II:

Introduction, Summary of Composite Hypothesis, Composite Hypothesis Testing (CHT),

CHT approaches – Bayesian Approach, Generalized Likelihood Approach, Performance of GLRT for Large Data Records, Equivalent Large Data Records Tests.

TEXT BOOKS:

1. Steven M. Kay, "Fundamentals of Statistical Signal Processing – Estimation Theory," Pearson, 2010.
2. Steven M. Kay, "Fundamentals of Statistical Signal Processing – Detection Theory," Pearson, 2010.

REFERENCE BOOKS:

3. Shanmugam and Breipohl, "*Detection of Signals in Noise and Estimation*,"John Wiley& Sons, 2004.
4. Mischa Schwartz, L.Shaw, "Signal Processing: Discrete Sprectral Analysis, Detection, and Estimation," McGraw Hill.

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(17D06201) EMBEDDED SYSTEM DESIGN

Course Objective:

- To study about current technologies, integration methods and hardware and software design concepts associated with processor in Embedded Systems.
- To study about a simple low power microcontrollers and their applications
- To get detail knowledge regarding testing and hardware software co- design issues pertaining to design of an Embedded System using low power microcontrollers

Course Outcome:

After completion of this course the students will be able to understand

- The issues relating to hardware and software design concepts associated with processor in Embedded Systems.
- The concept of low power microcontrollers.
- The hardware software co- design issues pertaining to design of an Embedded System using low power microcontrollers.

UNIT – I

Introductionto Embedded Electronic Systems and Microcontrollers:

An Embedded System-Definition, Embedded System Design and Development Life Cycle, An Introduction to Embedded system Architecture, The Embedded Systems Model, Embedded Hardware:The Embedded Board and the von Neumann Model, Embedded Processors: ISAArchitectureModels, Internal Processor Design, Processor Performance, Board Memory: Read-Only Memory (ROM), Random-Access Memory (RAM), Auxiliary Memory, Memory Management of External Memory and Performance, Approaches to Embedded Systems, Small Microcontrollers, Anatomy of a Typical Small Microcontroller, Small Microcontrollers Memory, Embedded Software, Introduction to small microcontroller (MSP430).

UNIT-II

MSP430 – I:

Architecture of the MSP430 Processor: Central Processing Unit, Addressing Modes, Constant Generator and Emulated Instructions, Instruction Set, Examples, Reflections on the CPU and Instruction Set, Resets, Clock System, Memory and Memory Organization.

Functions, Interrupts, and Low-Power Mode: Functions and Subroutines, Storage for Local Variables, Passing Parameters to a Subroutine and Returning a Result, Mixing C and Assembly Language, Interrupts, Interrupt Service Routines, Issues Associated with Interrupts, Low-Power Modes of Operation.

UNIT – III

MSP430 – II:

Digital Input, Output, and Displays: Parallel Ports, Digital Inputs, Switch Debounce, Digital Outputs, Interface between Systems, Driving Heavier Loads, Liquid Crystal Displays, Simple Applications of the LCD.

Timers: Watchdog Timer, Timer_A, Timer_A Modes, Timer_B,Timer_B Modes, Setting the Real-Time Clock, State Machines.

UNIT – IV

MSP430 Communication:

Communication Peripherals in the MSP430, Serial Peripheral Interface, SPI with the USI, SPI with the USCI, A Thermometer Using SPI Modes, Inter-integrated Circuit Bus(I^2C) and its operations, State Machines for I^2C Communication, A Thermometer Using I^2C, Asynchronous Serial Communication, Asynchronous Communication with the USCI_A, A Software UART Using Timer_A, Other Types of Communication.

UNIT – V

MSP430 Case Studies:

Introduction to Code Composer studio (CC Studio Ver. 6.1) a tutorial, A Study of blinking LED, Enabling LED using Switches, UART Communication, LCD interfacing, Interrupts, Analog to Digital Conversion, General Purpose input and output ports, I^2C.

TEXT BOOKS:

1. Tammy Noergaard "Embedded Systems Architecture: A Comprehensive Guide for Engineers and Programmers", Elsevier(Singapore) Pvt.Ltd.Publications, 2005.
2. John H. Davies "MSP430 Microcontroller Basics",Elsevier Ltd Publications, Copyright 2008.

REFERENCE BOOKS:

1. Manuel Jiménez Rogelio,PalomeraIsidoroCouvertier "Introduction to Embedded SystemsUsing Microcontrollers and the MSP430" Springer Publications, 2014.
2. Frank Vahid, Tony D. Givargis, "Embedded system Design: A Unified Hardware/Software Introduction", John Wily & Sons Inc.2002.
3. Peter Marwedel, "Embedded System Design", Science Publishers, 2007.
4. Arnold S Burger, "Embedded System Design", CMP Books, 2002.
5. Rajkamal, "Embedded Systems: Architecture, Programming and Design", TMH Publications,Second Edition, 2008.

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(17D06204) SENSORS AND ACTUATORS

Objectives

- To introduce the student to some basic principles and techniques of micro sensors and actuators
- understanding basic laws and phenomena on which operation of sensors and actuators-transformation of energy is based,

Outcomes: The student should after the course:

- Have knowledge about of the working principles and architecture of a large number of sensors and their elements.
- Be able to chose and use sensors and equipment for measuring mechanical quantities and temperature.
- Have knowledge about the architecture and working principles of the most common electrical motor types.
- Be able to chose and use electrical drives and actuators.
- Be able to cooperate in a active way with specialists in these areas.

UNIT -I:

Sensors / Transducers:

Principles – Classification – Parameters – Characteristics - Environmental Parameters (EP) – Characterization Mechanical and Electromechanical Sensors: Introduction – Resistive Potentiometer – Strain Gauge – Resistance Strain Gauge – Semiconductor Strain Gauges -Inductive Sensors: Sensitivity and Linearity of the Sensor –Types-Capacitive Sensors:– Electrostatic Transducer– Force/Stress Sensors Using Quartz Resonators – Ultrasonic Sensors

UNIT –II

Thermal Sensors:

Introduction – Gas thermometric Sensors – Thermal Expansion Type Thermometric Sensors – Acoustic Temperature Sensor – Dielectric Constant and Refractive Index thermosensors – Helium Low Temperature Thermometer – Nuclear Thermometer – Magnetic Thermometer – Resistance Change Type Thermometric Sensors –Thermoemf Sensors– Junction Semiconductor Types– Thermal Radiation Sensors –Quartz Crystal Thermoelectric Sensors – NQR Thermometry – Spectroscopic Thermometry – Noise Thermometry – Heat Flux Sensors Magnetic sensors: Introduction – Sensors and the Principles Behind – Magneto-resistive Sensors – Anisotropic Magnetoresistive Sensing – Semiconductor Magnetoresistors– Hall Effect and Sensors – Inductance and Eddy Current Sensors– Angular/Rotary Movement Transducers – Synchros – Synchro-resolvers - Eddy Current Sensors – Electromagnetic Flowmeter – Switching Magnetic Sensors SQUID Sensors

UNIT -III

Radiation Sensors:

Introduction – Basic Characteristics – Types of Photosensistors/Photo detectors– X-ray and Nuclear Radiation Sensors– Fiber Optic Sensors Electro analytical Sensors: Introduction – The Electrochemical Cell – The Cell Potential - Standard Hydrogen Electrode (SHE) – Liquid Junction and Other Potentials – Polarization – Concentration Polarization-– Reference Electrodes - Sensor Electrodes – Electro ceramics in Gas Media.

UNIT –IV

Smart Sensors:

Introduction – Primary Sensors – Excitation – Amplification – Filters – Converters – Compensation– Information Coding/Processing - Data Communication – Standards for Smart Sensor Interface – The Automation Sensors –Applications: Introduction – On-board Automobile Sensors (Automotive Sensors)– Home Appliance Sensors – Aerospace Sensors — Sensors for Manufacturing –Sensors for environmental Monitoring

UNIT -V:

Actuators:

Pneumatic and Hydraulic Actuation Systems- Actuation systems – Pneumatic and hydraulic systems - Directional Control valves – Presure control valves – Cylinders - Servo and proportional control valves – Process control valves – Rotary actuators Mechanical Actuation Systems- Types of motion – Kinematic chains – Cams – Gears – Ratchet and pawl – Belt and chain drives – Bearings – Mechanical aspects of motor selection Electrical Actuation Systems-Electrical systems -Mechanical switches – Solid-state switches Solenoids – D.C. Motors – A.C. motors – Stepper motors

TEXT BOOKS

1. D. Patranabis – “Sensors and Transducers” –PHI Learning Private Limited.
2. W. Bolton – “Mechatronics” –Pearson Education Limited.

REFERENCE BOOKS

1. Sensors and Actuators – D. Patranabis – 2nd Ed., PHI, 2013

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(17D38201) WIRELESS COMMUNICATIONS AND NETWORKS

Course Outcomes

After completion of the course students able to

- Understand concepts of wireless communication systems and their applications.
- Know about the mobile radio propagation techniques and detailed understanding in wireless mobile communication.
- Understand communication networks and detailed analysis of wireless communications networks.
- Understand the different protocols used for wireless communication systems and networks.

UNIT –I The Cellular Concept-System Design Fundamentals:

Introduction, Frequency Reuse, Channel Assignment Strategies, Handoff Strategies- Prioritizing Handoffs, Practical Handoff Considerations, Interference and system capacity – Co channel Interference and system capacity, Channel planning for Wireless Systems, Adjacent Channel interference , Power Control for Reducing interference, Trunking and Grade of Service, Improving Coverage & Capacity in Cellular Systems- Cell Splitting, Sectoring .

UNIT –II Mobile Radio Propagation:

Large-Scale Path Loss: Introduction to Radio Wave Propagation, Free Space Propagation Model, Relating Power to Electric Field, The Three Basic Propagation Mechanisms, Reflection-Reflection from Dielectrics, Brewster Angle, Reflection from prefect conductors, Ground Reflection (Two-Ray) Model, Diffraction-Fresnel Zone Geometry, Knife-edge Diffraction Model, Multiple knife-edge Diffraction, Scattering, Outdoor Propagation Models- LongleyRyce Model, Okumura Model, Hata Model, PCS Extension to Hata Model, Walfisch and Bertoni Model, Wideband PCS Microcell Model, Indoor Propagation Models-Partition losses (Same Floor), Partition losses between Floors, Log-distance path loss model, Ericsson Multiple Breakpoint Model, Attenuation Factor Model, Signal penetration into buildings, Ray Tracing and Site Specific Modeling.

UNIT –III Mobile Radio Propagation:

Small –Scale Fading and Multipath: Small Scale Multipath propagation-Factors influencing small scale fading, Doppler shift, Impulse Response Model of a multipath channel- Relationship between Bandwidth and Received power, Small-Scale Multipath Measurements-Direct RF Pulse System, Spread Spectrum Sliding Correlator Channel Sounding, Frequency Domain Channels Sounding, Parameters of Mobile Multipath Channels-Time Dispersion Parameters, Coherence Bandwidth, Doppler Spread and Coherence Time, Types of Small-Scale Fading-Fading effects Due to Multipath Time Delay Spread, Flat fading, Frequency selective fading, Fading effects Due to Doppler Spread-Fast fading, slow fading, Statistical Models for multipath Fading Channels-Clarke's model for flat fading, spectral shape due to Doppler spread in Clarke's model, Simulation of Clarke and Gans Fading Model, Level crossing and fading statistics, Two-ray Rayleigh Fading Model.

UNIT –IV Equalization and Diversity:

Introduction, Fundamentals of Equalization, Training A Generic Adaptive Equalizer, Equalizers in a communication Receiver, Linear Equalizers, Non linear Equalization-Decision Feedback Equalization (DFE), Maximum Likelihood Sequence Estimation (MLSE) Equalizer, Algorithms for adaptive equalization-Zero Forcing Algorithm, Least Mean Square Algorithm, Recursive least squares algorithm. Diversity Techniques-Derivation of selection Diversity improvement, Derivation of Maximal Ratio Combining improvement, Practical Space Diversity Consideration-Selection Diversity, Feedback or Scanning Diversity, Maximal Ratio Combining, Equal Gain Combining, Polarization Diversity, Frequency Diversity, Time Diversity, RAKE Receiver.

UNIT –V Wireless Networks:

Introduction to wireless Networks, Advantages and disadvantages of Wireless Local Area Networks, WLAN Topologies, WLAN Standard IEEE 802.11,IEEE 802.11 Medium Access Control, Comparision of IEEE 802.11 a,b,g and n standards, IEEE 802.16 and its enhancements, Wireless PANs, HiperLan, WLL.

TEXT BOOKS

1. Wireless Communications, Principles, Practice – Theodore, S. Rappaport, 2nd Ed., 2002, PHI.
2. Wireless Communications-Andrea Goldsmith, 2005 Cambridge University Press.
3. Mobile Cellular Communication – GottapuSasibhushanaRao, Pearson Education, 2012.

REFERENCE BOOKS

1. Principles of Wireless Networks – KavehPahLaven and P. Krishna Murthy, 2002, PE
2. Wireless Digital Communications – KamiloFeher, 1999, PHI.
3. Wireless Communication and Networking – William Stallings, 2003, PHI.
4. Wireless Communication – UpenDalal, Oxford Univ. Press 5. Wireless Communications and Networking – Vijay K. Gary, Elsevier

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(17D38204) SOFTWARE DEFINED RADIO

Elective-III

Course Objective:

- To study about requirements, benefits and different models for Software Defined Radio
- To study in detail about Soft ware Defined Radio Architectures for performance optimization
- To get complete knowledge regarding functioning of different blocks and techniques associated with Software Defined Radio.

Course Outcome:

After completion of this course the students will be able to

- Analyze requirements, benefits and different models for Software Defined Radio.
- Understand in detail about Soft ware Defined Radio Architectures for performance optimization.
- Gets complete knowledge regarding functioning of different blocks and techniques associated with Software Defined Radio.

UNIT-I

Requirement for Software defined radio, Benefits of multi-standard terminals, Operational requirements, models for SDR, Smart antenna systems,

UNIT - II

Software defined radio architectures, Hardware specifications, Digital aspects of Software defined radio, Current technology limitations, minimum power consumption, ADC performance trends

UNIT-III

Flexible RF receiver architectures, Digital receiver, Single carrier and multi-carrier designs, undersampling, oversampling, Noise figure, Receiver sensitivity, ADC spurious signals

UNIT-IV

Multiband Flexible receiver design, RF Transmit / receive switch, Image rejection mixing, Dynamic range enhancement, Feed forward techniques, cascaded non-linearity techniques

UNIT - V

Flexible transmitters,, Power amplifiers, Analog quadrature upconvertion, Interpolated bandpassupconversion, PLL based modulator transmitter, All-pass filtering, Polyphase filtering

TEXT BOOKS:

1. P Kenington, "RF and Baseband Techniques for Software Defined Radio", Artec House, 2005
2. JoukoVanakka, "Digital Synthesizers And Transmitter For Software Radio", Springer, 2005

REFERENCE BOOKS:

3. Wally H. W. Tuttlebee**, "**Software Defined Radio: Baseband Technologies for 3G Handsets and Base stations",John Wiley &sons , 2003

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(17D06103) ADVANCED COMPUTER ARCHITECTURE
Elective-III

Course objective:

- To study about various parallel computer models and also to study the program and network properties
- To study the concepts of pipelining and super scalar techniques.
- To study about architectures of multi processors and multi computers

Course ***Outcome:***

After completion of the course the students will be able to

- Know about different parallel computer models and their network properties.
- Understand about different concepts related to pipelining and super scalar techniques.
- Get complete knowledge regarding multi processors and multi computers.

UNIT - I

Parallel Computer Models – System attributes to performance, Multiprocessors and Multicomputers, Classifications of Architectures, Multivector and SIMD Computers, Architecture development tracks

UNIT - II

Program and Network Properties- Conditions for parallelism, Program partitioning and Scheduling, Program flow mechanisms, System interconnect architectures, Performance metrics and measures, Parallel Processing Applications

UNIT-III

Processors and Memory Hierarchy- Advanced Processor Technology, Superscalar and Vector processors, Memory hierarchy technology, Virtual Memory, Backplane bus systems, Cache memory organizations, Shared memory organizations

UNIT - IV

Pipelining and Superscalar Techniques Linear Pipeline processors, Nonlinear pipeline processors, Instruction pipeline design, Arithmetic pipeline design, Superscalar and Super Pipeline Design

UNIT- V

Multiprocessors and Multicomputers Multiprocessor System Interconnects, Cache Coherence and Synchronization mechanisms, Three generations of Multicomputers, Message passing mechanisms, Vector Processing principles, Principles of Multithreading

TEXT BOOKS:

1. Hwang kai, "Advanced Computer Architecture", McGraw-Hill, 2001.
2. Patterson, Davidand Hennessy John**,** Morgn Kaufmann, "Computer Architecture",2001.

REFERENCE BOOKS:

1. William Stallings, Computer Organization and Architecture, 8th Edition, Prentice-Hall India, 2010.
2. David A Patterson and John L. Hennesey, Computer Organization and Design, 4th Edition, Elsevier India, 2011.
3. Andrew S Tanenbaum and James R Goodman, Structured Computer Organization, 5th Edition PrenticeHall India, 2009.

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(17D70202) SOFT COMPUTING TECHIQUES
Elective-III

Course Outcomes: After going through this course, a student shall be able

- To know about the basics of soft computing techniques and also their use in some real life situations.
- To solve the problems using neural networks techniques.
- To find the solution using different fuzzy logic techniques
- To use the genetic algorithms for different modelling
- To integrate the various soft computing techniques

UNIT –I

Introduction:

Approaches to intelligent control, Architecture for intelligent control, Symbolic reasoning system, Rule-based systems, the AI approach, Knowledge representation - Expert systems.

UNIT –II

Artificial Neural Networks:

Concept of Artificial Neural Networks and its basic mathematical model, McCulloch-Pitts neuron model, simple perceptron, Adaline and Madaline, Feed-forward Multilayer Perceptron, Learning and Training the neural network, Data Processing: Scaling, Fourier transformation, principal-component analysis and wavelet transformations, Hopfield network, Self-organizing network and Recurrent network, Neural Network based controller.

UNIT –III

Fuzzy Logic System:

Introduction to crisp sets and fuzzy sets, basic fuzzy set operation and approximate reasoning, Introduction to fuzzy logic modeling and control, Fuzzification, inferencing and defuzzification, Fuzzy knowledge and rule bases, Fuzzy modeling and control schemes for nonlinear systems, Selforganizing fuzzy logic control, Fuzzy logic control for nonlinear time delay system.

UNIT –IV:

Genetic Algorithm:

Basic concept of Genetic algorithm and detail algorithmic steps, Adjustment of free parameters, Solution of typical control problems using genetic algorithm, Concept on some other search techniques like Tabu search and Ant-colony search techniques for solving optimization problems.

UNIT –V

Applications:

GA application to power system optimisation problem, Case studies: Identification and control of linear and nonlinear dynamic systems using MATLAB-Neural Network toolbox, Stability analysis of Neural-

Network interconnection systems, Implementation of fuzzy logic controller using MATLAB fuzzy-logic toolbox, Stability analysis of fuzzy control systems.

TEXT BOOKS

1. Introduction to Artificial Neural Systems - Jacek.M.Zurada, Jaico Publishing House, 1999.
2. Neural Networks and Fuzzy Systems - Kosko, B., Prentice-Hall of India Pvt. Ltd., 1994.

REFERENCE BOOKS

1. Fuzzy Sets, Uncertainty and Information - Klir G.J. &Folger T.A., Prentice-Hall of India Pvt. Ltd., 1993.
2. Fuzzy Set Theory and Its Applications - Zimmerman H.J. Kluwer Academic Publishers, 1994.
3. Introduction to Fuzzy Control - Driankov, Hellendroon, Narosa Publishers.
4. Artificial Neural Networks - Dr. B. Yagananarayana, 1999, PHI, New Delhi.
5. Elements of Artificial Neural Networks - KishanMehrotra, Chelkuri K. Mohan, Sanjay Ranka, Penram International.
6. Artificial Neural Network –Simon Haykin, 2nd Ed., Pearson Education.
7. Introduction Neural Networks Using MATLAB 6.0 - S.N. Shivanandam, S. Sumati, S. N. Deepa,1/e, TMH, New Delhi.

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(17D70203) DIGITAL SIGNAL PROCESSORS AND ARCHITECTURES

Elective-IV

Course Outcomes:

- To be able to develop the program for fixed and floating point DSP processors based on the design issues.
- To be able to design and develop real time implementations on DSP algorithms.
- Ability to design IIR and FIR filters.
- To apply the fast transforms for the analysis of DSP systems.
- To be able to realize and implement a suitable structure for FIR and IIR Filters.
- To be able to design adaptive filter algorithms

UNIT –I

Introduction to Digital Signal Processing: Introduction, a Digital signal-processing system, the sampling process, discrete time sequences. Discrete Fourier Transform (DFT) and Fast Fourier Transform (FFT), Linear time-invariant systems, Digital filters, Decimation and interpolation. Computational Accuracy in DSP Implementations: Number formats for signals and coefficients in DSP systems, Dynamic Range and Precision, Sources of error in DSP implementations, A/D Conversion errors, DSP Computational errors, D/A Conversion Errors, Compensating filter.

UNIT –II

Architectures for Programmable DSP Devices: Basic Architectural features, DSP Computational Building Blocks, Bus Architecture and Memory, Data Addressing Capabilities, Address Generation UNIT, Programmability and Program Execution, Speed Issues, Features for External interfacing.

UNIT –III

Programmable Digital Signal Processors: Commercial Digital signal-processing Devices, Data Addressing modes of TMS320C54XX DSPs, Data Addressing modes of TMS320C54XX Processors, Memory space of TMS320C54XX Processors, Program Control, TMS320C54XX Instructions and Programming, On-Chip Peripherals, Interrupts of TMS320C54XX Processors, Pipeline Operation of TMS320C54XX Processors.

UNIT –IV

Analog Devices Family of DSP Devices: Analog Devices Family of DSP Devices – ALU and MAC block diagram, Shifter Instruction, Base Architecture of ADSP 2100, ADSP-2181 high performance Processor. Introduction to Black fin Processor - The Black fin Processor, Introduction to Micro Signal Architecture, Overview of Hardware Processing Units and Register files, Address Arithmetic Unit, Control Unit, Bus Architecture and Memory, Basic Peripherals.

UNIT –V

Interfacing Memory and I/O Peripherals to Programmable DSP Devices: Memory space organization, External bus interfacing signals, Memory interface, Parallel I/O interface, Programmed I/O, Interrupts and I/O, Direct memory access (DMA).

TEXT BOOKS:

1. Digital Signal Processing – Avtar Singh and S. Srinivasan, Thomson Publications, 2004.
2. A Practical Approach To Digital Signal Processing - K Padmanabhan, R. Vijayarajeswaran, Ananthi. S, New Age International, 2006/2009
3. Embedded Signal Processing with the Micro Signal Architecture: Woon-SengGan, Sen M. Kuo, Wiley-IEEE Press, 2007

REFERENCE BOOKS:

1. Digital Signal Processors, Architecture, Programming and Applications – B. Venkataramani and M. Bhaskar, 2002, TMH.
2. Digital Signal Processing – Jonatham Stein, 2005, John Wiley.
3. DSP Processor Fundamentals, Architectures & Features – Lapsley et al. 2000, S. Chand & Co.
4. Digital Signal Processing Applications Using the ADSP-2100 Family by The Applications Engineering Staff of Analog Devices, DSP Division, Edited by Amy Mar, PHI
5. The Scientist and Engineer's Guide to Digital Signal Processing by Steven W. Smith, Ph.D., California Technical Publishing, ISBN 0-9660176-3-3, 1997
6. Embedded Media Processing by David J. Katz and Rick Gentile of Analog Devices, Newnes , ISBN 0750679123, 2005

JAWAHARLAL NEHRU TECHNOLOGICAL UNIVERSITY ANANTAPUR

M.Tech I year II Semester (ECE)	**L**	**T**	**P**	**C**
	3	**0**	**0**	**3**

(17D70204) ELECTROMAGNETIC INTERFERENCE AND ELECTROMAGNETIC COMPATIBILITY (EMI / EMC)

Elective-IV

Course Objective:
To understand EMI sources, EMI problems and their solutions at PCB level, and also to understand sub system level design and to measure the emission, immunity level from different systems to couple with the prescribed EMC standards.

Course Outcome: Upon successful completion of this course, students should be able to:

- diagnose and solve basic electromagnetic compatibility problems.
- design electronic systems that function without errors or problems related to electromagnetic compatibility.
- design the Cable routing & connection and understand the Interconnection Techniques d. design high speed Printed Circuit board with minimum interference.
- design a EMI free system.

UNIT -I:
Introduction, Natural and Nuclear Sources of EMI / EMC:
Electromagnetic environment, History, Concepts, Practical experiences and concerns, frequency spectrum conservations, An overview of EMI / EMC, Natural and Nuclear sources of EMI.

UNIT -II:
EMI from Apparatus, Circuits and Open Area Test Sites:
Electromagnetic emissions, Noise from relays and switches, Non-linearities in circuits, passive intermodulation, Cross talk in transmission lines, Transients in power supply lines, Electromagnetic interference (EMI), Open area test sites and measurements.

UNIT -III:
Radiated and Conducted Interference Measurements and ESD:
Anechoic chamber, TEM cell, GH TEM Cell, Characterization of conduction currents / voltages, Conducted EM noise on power lines, Conducted EMI from equipment, Immunity to conducted EMI detectors and measurements, ESD, Electrical fast transients / bursts, Electrical surges.

UNIT -IV:
Grounding, Shielding, Bonding and EMI filters:
Principles and types of grounding, Shielding and bonding, Characterization of filters, Power lines filter design.

UNIT -V:
Cables, Connectors, Components and EMC Standards:
EMI suppression cables, EMC connectors, EMC gaskets, Isolation transformers, optoisolators, National / International EMC standards.

TEXT BOOKS:

1. Engineering Electromagnetic Compatibility - Dr. V.P. Kodali, IEEEPublication, Printed in India by S. Chand & Co. Ltd., New Delhi, 2000.
2. Electromagnetic Interference and Compatibility IMPACT series, IIT – Delhi, Modules 1– 9.

REFERENCE BOOKS:

1. Introduction to Electromagnetic Compatibility - Ny, John Wiley, 1992, by C.R. Pal.

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(17D38202) INTERNET OF THINGS
Elective-IV

Course description and objectives:

Students will be explored to the interconnection and integration of the physical world and the cyber space. They are also able to design & develop IOT Devices.

Course Outcomes:

- Able to understand the application areas of IOT
- Able to realize the revolution of Internet in Mobile Devices, Cloud & Sensor Networks
- Able to understand building blocks of Internet of Things and characteristics.

Unit I

Introduction & Concepts: Introduction to Internet of Things, Physical Design of IOT, Logical Design of IOT, IOT Enabling Technologies, IOT Levels.

Unit II

Domain Specific IOTs: Home Automation, Cities, Environment, Energy, Retail, Logistics, Agriculture, Industry, Health & Life Style.

Unit III

M2M & System Management with NETCONF-YANG: M2M, Difference between IOT and M2M, SDN and NFV for IOT, Software defined Networking, Network Function Virtualization, Need for IOT Systems Management, Simple Network Management Protocol, Limitations of SNMP, Network Operator Requirements, NETCONF, YANG, IOT Systems management with NETCONF-YANG.

Unit IV

Developing Internet of Things & Logical Design using Python: Introduction, IOT Design Methodology, Installing Python, Python Data Types & Data Structures, Control Flow, Functions, Modules, Packages, File Handling, Date/ Time Operations, Classes, Python Packages

Unit V

IOT Physical Devices & Endpoints: What is an IOT Device, Exemplary Device, Board, Linux on Raspberry Pi, Interfaces, and Programming & IOT Devices.

TEXT BOOKS:

1. Vijay Madisetti, ArshdeepBahga," Internet of Things A Hands-On- Approach",2014, ISBN:978 0996025515

REFERENCE BOOKS:

1. Adrian McEwen, “Designing the Internet of Things”, Wiley Publishers, 2013, ISBN: 978-1-118-43062-0
2. Daniel Kellmereit, “The Silent Intelligence: The Internet of Things”. 2013, ISBN

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(17D38108) ADVANCED DIGITAL SIGNAL PROCESSING LAB

Note:
A. Minimum of 10 Experiments have to be conducted
B. All Simulations are be carried out using MATLAB/DSP Processors/Lab view Software & DSP Kits

1. Study of various addressing modes of DSP using simple programming examples
2. Generation of waveforms using recursive/filter methods
3. Sampling of input signal and display
4. Implementation of Linear and Circular Convolution for sinusoidal signals
5. Framing & windowing of speech signal.
6. Finding voiced & unvoiced detection for each frame of speech signal.
7. IIR Filter implementation using probe points
8. Implementation of FIR filters on DSP processor
9. Loop back using DSK kit
10. Real time signal enhancement using Adaptive Filter.
11. Representation of different Q-formats using GEL function
12. Verification of Finite word length effects (Overflow, Coefficient Quantization, Scaling and Saturation mode in DSP processors)
13. Image enhancement using spatial & frequency domain
14. Implementation of Image segmentation techniques
15. Extraction of frames from Video signal

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M.Tech I year II Semester (ECE)	**L**	**T**	**P**	**C**
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(17D38208) EMBEDDED SYSTEM DESIGN LAB

Course Outcomes:

After Completion of this Lab, Students will be able to

- Design and Implement basic circuits that are used in Embedded systems.
- Develop code using appropriate tools.
- Test the circuit performance with standard benchmark circuits.

List of Experiments

PART – A

Using Embedded C

Note: Any 10 Programs form the following

1. Write a simple program to print "hello world"
2. Write a simple program to show a delay.
3. Write a loop application to copy values from P1 to P2
4. Write a c program for counting the no of times that a switch is pressed & released.
5. Illustrate the use of port header file (port M) using an interface consisting of a keypad and liquid crystal display.
6. Write a program to create a portable hardward delay.
7. Write a c program to test loop time outs.
8. Write a c program to test hardware based timeout loops.
9. Develop a simple EOS showing traffic light sequencing.
10. Write a program to display elapsed time over RS-232 link.
11. Write a program to drive SEOS using Timer 0.
12. Develop software for milk pasteurization system.

PART – B

Note. Any 6 Programs from the following (Experiment – 1 is mandatory)

1. A Study of Code Composer Studio (CC Studio Latest Version)
2. Flashing a light by a software delay.
3. Displaying Characters on LCD.
4. Serial Communication using UART.
5. Basic Input and Output using MSP430 UART.
6. Interrupt Handling using MSP430.
7. Analog to Digital Conversion using MSP430.
8. Interfacing external Devices to GPIO Ports

Equipments Required:

1. Computer with latest configurations
2. Code Composer Studio v6.1 (Preferably Latest version)
3. MSP430/ARM based Hardware kits and add-on boards.

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M.Tech III semester (ECE) **L T P C**

4 0 0 4

(17D20301) RESEARCH METHODOLOGY
(Elective V-OPEN ELECTIVE)

UNIT I

Meaning of Research – Objectives of Research – Types of Research – Research Approaches – Guidelines for Selecting and Defining a Research Problem – research Design – Concepts related to Research Design – Basic Principles of Experimental Design.

UNIT II

Sampling Design – steps in Sampling Design –Characteristics of a Good Sample Design – Random Sampling Design.
Measurement and Scaling Techniques-Errors in Measurement – Tests of Sound Measurement – Scaling and Scale Construction Techniques – Time Series Analysis – Interpolation and Extrapolation.
Data Collection Methods – Primary Data – Secondary data – Questionnaire Survey and Interviews.

UNIT III

Correlation and Regression Analysis – Method of Least Squares – Regression vs Correlation – Correlation vs Determination – Types of Correlations and Their Applications

UNIT IV

Statistical Inference: Tests of Hypothesis – Parametric vs Non-parametric Tests – Hypothesis Testing Procedure – Sampling Theory – Sampling Distribution – Chi-square Test – Analysis of variance and Co-variance – Multi-variate Analysis.

UNIT V

Report Writing and Professional Ethics: Interpretation of Data – Report Writing – Layout of a Research Paper – Techniques of Interpretation- Making Scientific Presentations in Conferences and Seminars – Professional Ethics in Research.

Text Books:

Research Methodology:Methods And Techniques – C.R.Kothari, 2nd Edition,New Age International Publishers.
Research Methodology: A Step By Step Guide For Beginners- Ranjit Kumar, Sage Publications (Available As Pdf On Internet)
Research Methodology And Statistical Tools – P.Narayana Reddy And G.V.R.K.Acharyulu, 1st Edition,Excel Books,New Delhi.

REFERENCES:

1. Scientists Must Write - Robert Barrass (Available As Pdf On Internet)
2. Crafting Your Research Future –Charles X. Ling And Quiang Yang (Available As Pdf On Internet)

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M.Tech III semester (ECE) **L T P C**

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(17D20302) HUMAN VALUES AND PROFESSIONAL ETHICS

(Elective V-OPEN ELECTIVE)

Unit I:

HUMAN VALUES:Morals, Values and Ethics-Integrity-Work Ethic-Service learning – Civic Virtue – Respect for others – Living Peacefully – Caring – Sharing – Honesty - Courage- Co Operation – Commitment – Empathy –Self Confidence Character – Spirituality.

Unit II:

ENGINEERING ETHICS: Senses of Engineering Ethics- Variety of moral issues – Types of inquiry – Moral dilemmas – Moral autonomy –Kohlberg‟s theory- Gilligan‟s theory- Consensus and controversy – Models of professional roles- Theories about right action- Self interest - Customs and religion –Uses of Ethical theories – Valuing time –Co operation – Commitment.

Unit III :

ENGINEERING AS SOCIAL EXPERIMENTATION: Engineering As Social Experimentation – Framing the problem – Determining the facts – Codes of Ethics – Clarifying Concepts – Application issues – Common Ground - General Principles – Utilitarian thinking respect for persons.

UNIT IV:

ENGINEERS RESPONSIBILITY FOR SAFETY AND RISK: Safety and risk – Assessment of safety and risk – Risk benefit analysis and reducing riskSafety and the Engineer- Designing for the safety- Intellectual Property rights(IPR).

UINIT V:

GLOBAL ISSUES: Globalization – Cross culture issues- Environmental Ethics – Computer Ethics – Computers as the instrument of Unethical behavior – Computers as the object of Unethical acts – Autonomous Computers- Computer codes of Ethics – Weapons Development - Ethics .

Text Books :

1. “Engineering Ethics includes Human Values” by M.Govindarajan, S.Natarajan and V.S.SenthilKumar-PHI Learning Pvt. Ltd-2009.
2. “Engineering Ethics” by Harris, Pritchard and Rabins, CENGAGE Learning, India Edition, 2009.
3. “Ethics in Engineering” by Mike W. Martin and Roland Schinzinger – Tata McGrawHill– 2003.
4. “Professional Ethics and Morals” by Prof.A.R.Aryasri, Dharanikota Suyodhana-Maruthi Publications.

5. “Professional Ethics and Human Values” by A.Alavudeen, R.Kalil Rahman and M.Jayakumaran , Laxmi Publications.

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M.Tech III semester (ECE)

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(17D20303) INTELLECTUAL PROPERTY RIGHTS
(Elective V-OPEN ELECTIVE)

UNIT – I

Introduction To Intellectual Property: Introduction, Types Of Intellectual Property, International Organizations, Agencies And Treaties, Importance Of Intellectual Property Rights.

UNIT – II

Trade Marks : Purpose And Function Of Trade Marks, Acquisition Of Trade Mark Rights, Protectable Matter, Selecting And Evaluating Trade Mark, Trade Mark Registration Processes.

UNIT – III

Law Of Copy Rights : Fundamental Of Copy Right Law, Originality Of Material, Rights Of Reproduction, Rights To Perform The Work Publicly, Copy Right Ownership Issues, Copy Right Registration, Notice Of Copy Right, International Copy Right Law.

Law Of Patents : Foundation Of Patent Law, Patent Searching Process, Ownership Rights And Transfer

UNIT – IV

Trade Secrets : Trade Secrete Law, Determination Of Trade Secrete Status, Liability For Misappropriations Of Trade Secrets, Protection For Submission, Trade Secrete Litigation.

Unfair Competition : Misappropriation Right Of Publicity, False Advertising.

UNIT – V

New Development Of Intellectual Property: New Developments In Trade Mark Law ; Copy Right Law, Patent Law, Intellectual Property Audits.

International Overview On Intellectual Property, International – Trade Mark Law, Copy Right Law, International Patent Law, International Development In Trade Secrets Law.

TEXT BOOKS & REFERENCES:

1. Intellectual Property Right, Deborah. E. Bouchoux, Cengage Learing.
2. Intellectual Property Right – Nleashmy The Knowledge Economy, Prabuddha Ganguli, Tate Mc Graw Hill Publishing Company Ltd.,